

Figure 1

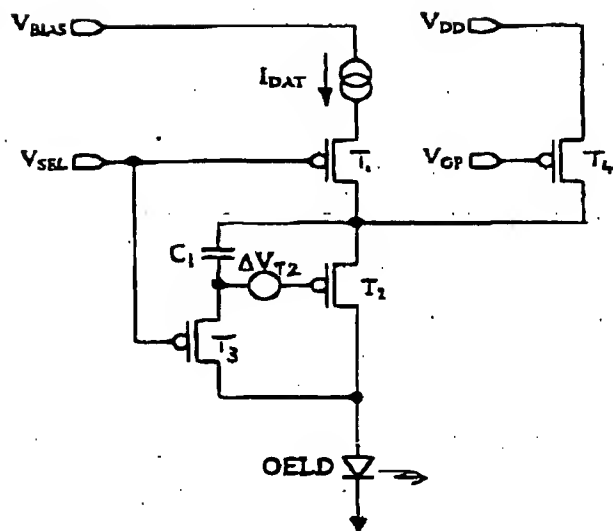


Figure 2A

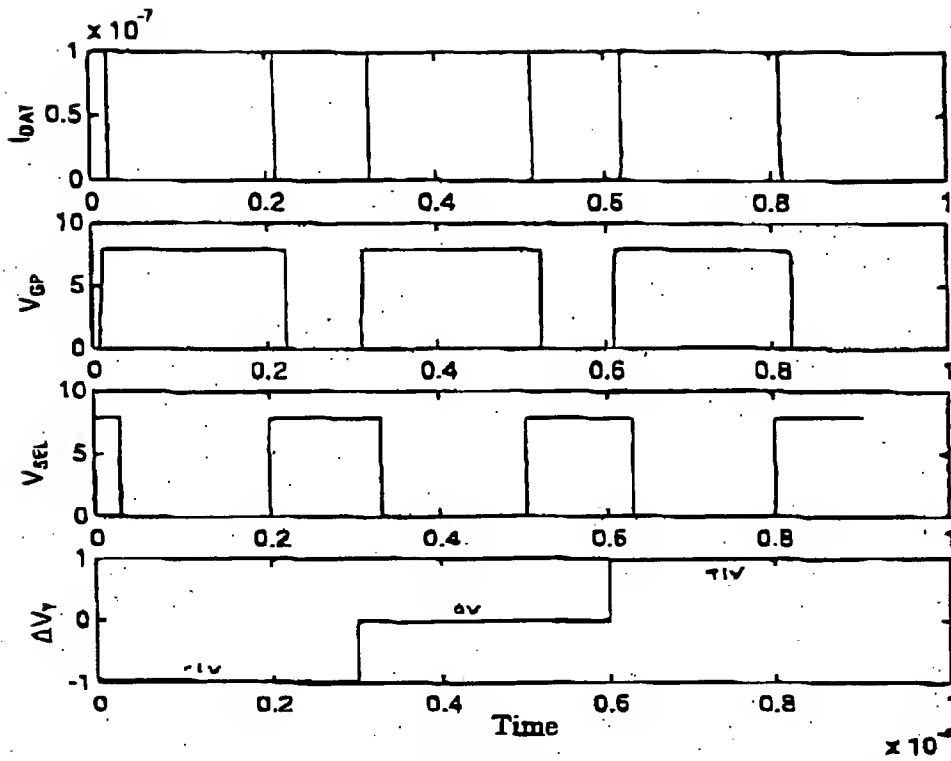


Figure 2B

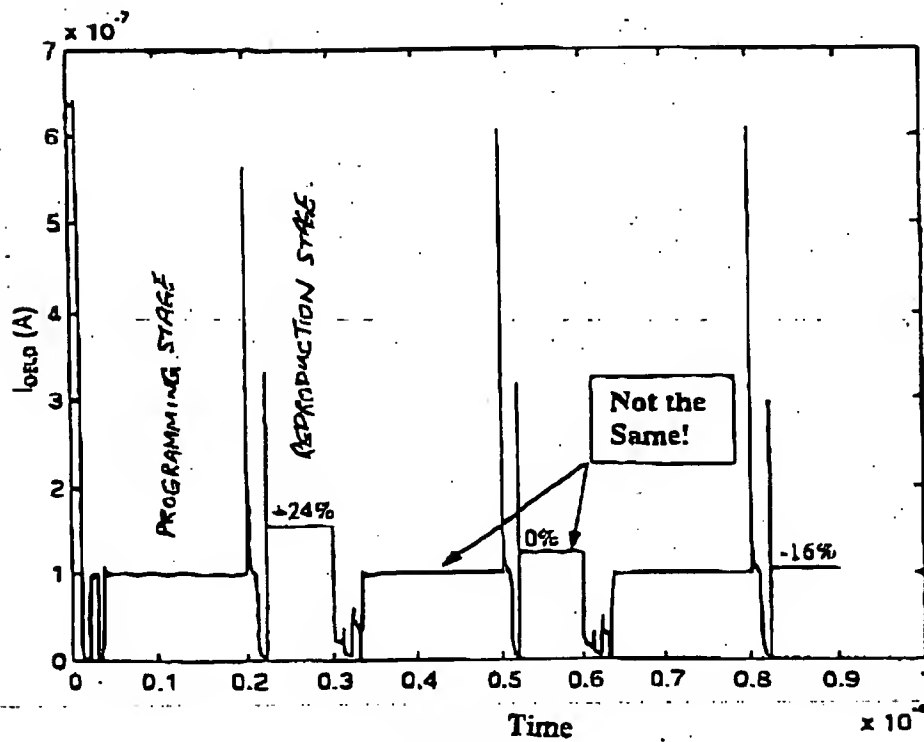


Figure 2C

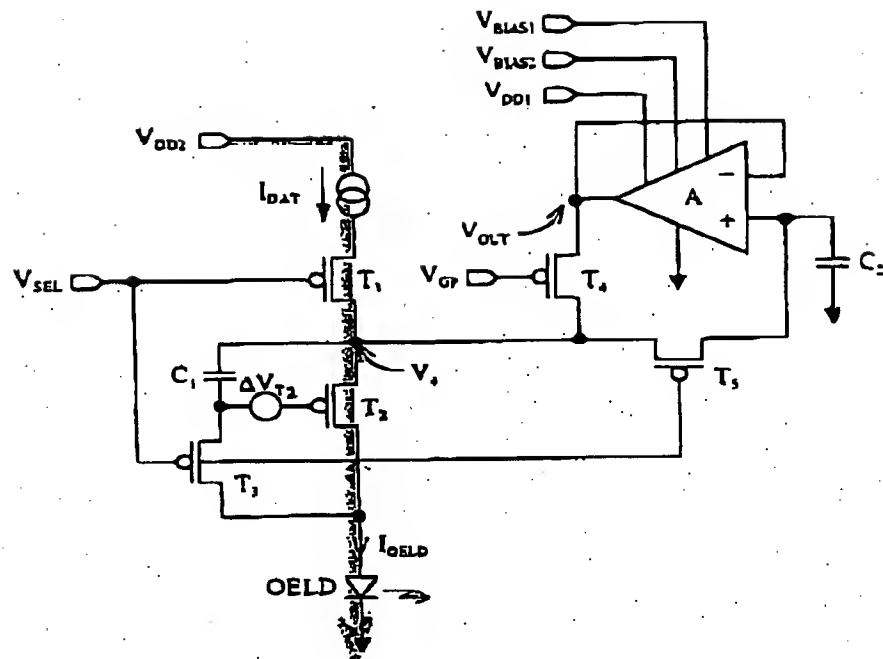


Figure 3

| | |
|-------------------------------|--|
| Open-Loop Gain (OLG) | >20 |
| V_{DD} | 9 V |
| V_{SS} | 0 V |
| V_{OUT} | 1 V - 8 V |
| Inverting Input (V_-) | $\approx V_{OUT}$ |
| Non-Inverting Input (V_+) | 1 V - 8 V |
| Max Output Current | 1 μ A |
| Settle Time | <20 μ s |
| Offset Voltage | Minimal |
| Quiescent Current | Minimal |
| Space Occupation | < 210 \times 70 μ m ² |

Single Power Supply
Rail to Rail

Figure 4

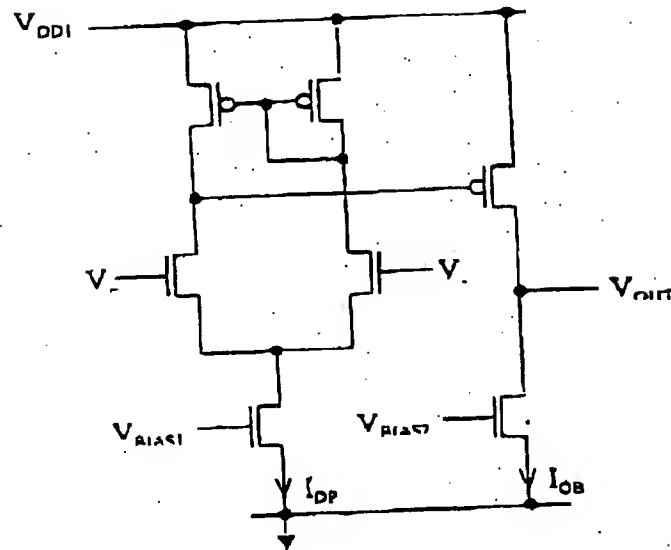


Figure 5

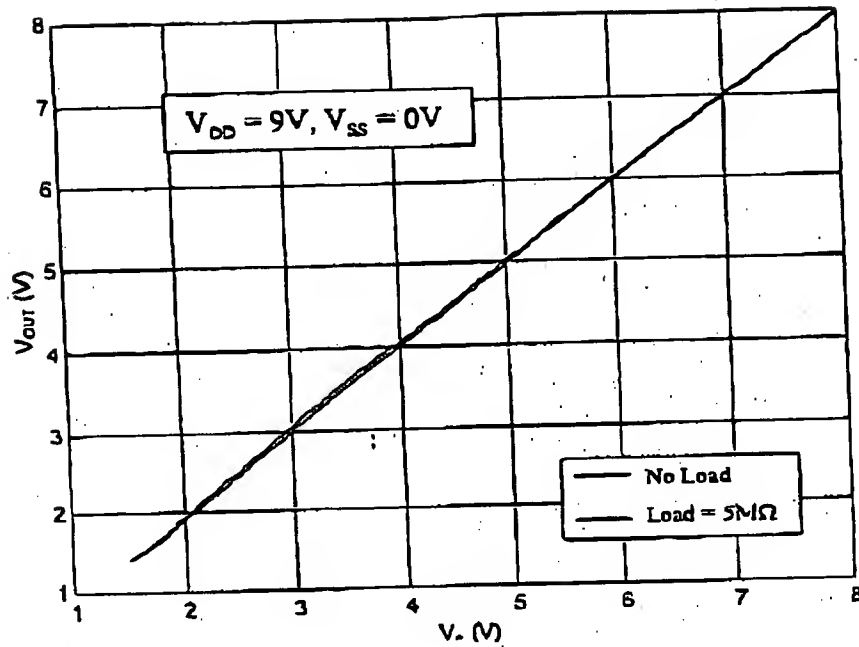


Figure 6

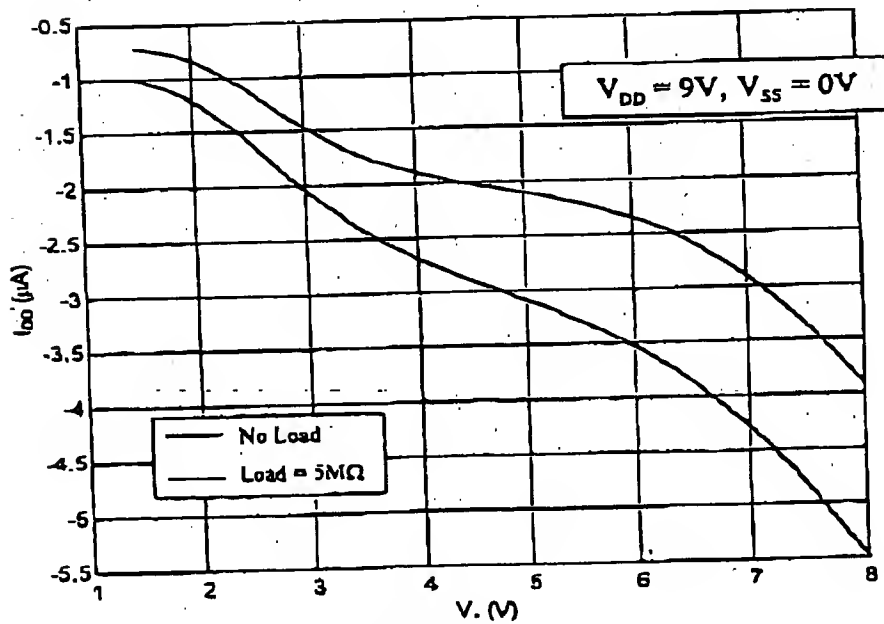


Figure 7

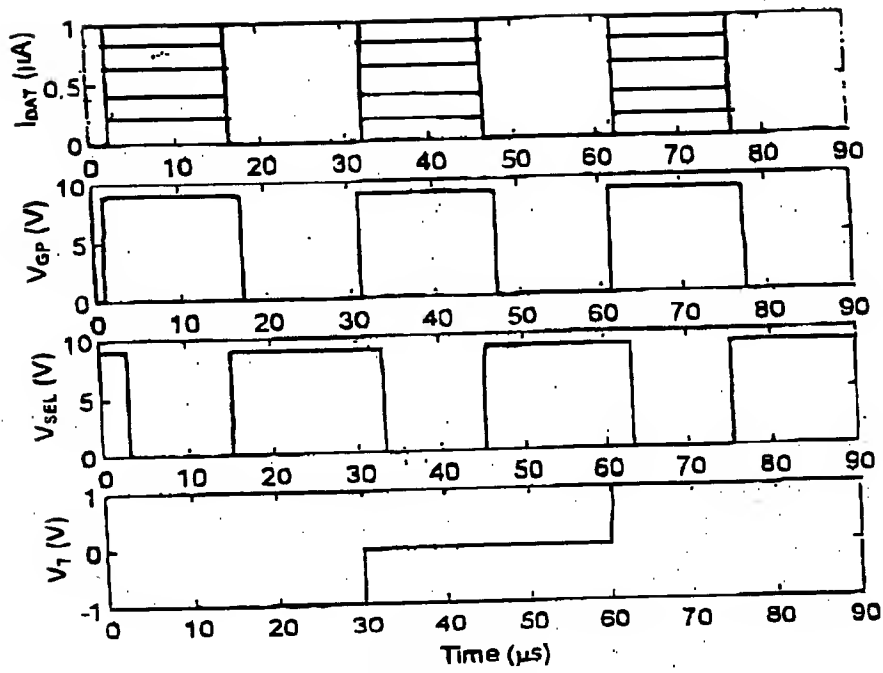


Figure 8

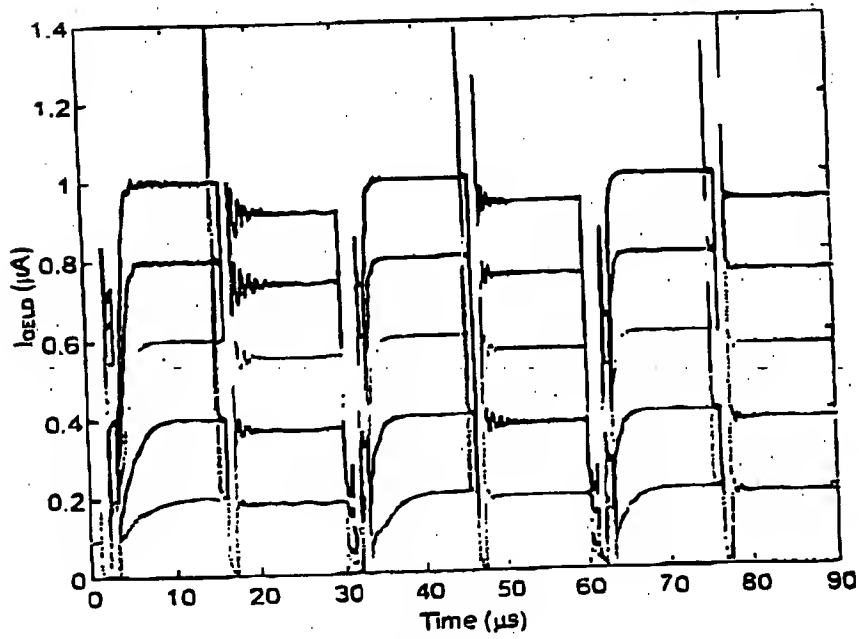


Figure 9